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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/001,624	10/24/2001	Satoshi Kawai	9281-4213	1193
75	590 05/29/2003			
Brinks Hofer Gilson & Lione			EXAMINER	
P.O. Box 10395 Chicago, IL 60610			LEE, BENNY T	
			ART UNIT	PAPER NUMBER
	,		2817	

DATE MAILED: 05/29/2003

Please find below and/or attached an Office communication concerning this application or proceeding.



## UNITED STATES DEPARTMENT OF COMMERCE Patent and Tridemark Office Address: COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231

SERIAL NUMBER	FILING DATE		FIRST NAMED APPLICANT		, A1	ATTORNEY DOCKET NO.	
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				<u> </u>	DATE MAN ED.		

This is a communication from the examiner in charge of your application,

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This application has been examined Responsive to communication filed on 240d	This action is made final.
A shortened statutory period for response to this action is set to expire Thra month(s),  Failure to respond within the period for response will cause the application to become abandoned.	_ days from the date of this letter.
Part 1THE FOLLOWING ATTACHMENT(S) ARE PART OF THIS ACTION:	
	atent Drawing, PTO-948.  formal Patent Application, Form PTO-152
Part II SUMMARY OF ACTION	. *
1. Ctaims 1-6	are pending in the application.
Of the above, claims	are withdrawn from consideration.
2. Claims	have been cancelled.
3.: Claims	are allowed.
4. Ctaims	are rejected.
Claims	are objected to.
6. Claimsarc	e subject to restriction or election requirement.
<ol> <li>This application has been filed with informal drawings which are acceptable for examina matter is indicated.</li> </ol>	tion purposes until such time as allowable subject
8. Allowable subject matter having been indicated, formal drawings are required in response	e to this Office action.
9. The corrected or substitute drawings have been received on  not acceptable (see explanation).	These drawings are acceptable;
10. The proposed drawing correction and/or the proposed additional or substitute sh has (have) been approved by the examiner. disapproved by the examiner (see e.	
11. The proposed drawing correction, filed, has been approved.	ed. disapproved (see explanation). However,
the Patent and Trademark Office no longer makes drawing changes. It is now applicant' corrected. Corrections MUST be effected in accordance with the instructions set forth c EFFECT DRAWING CHANGES", PTO-1474.	on the attached letter "INFORMATION ON HOW To
Acknowledgment is made of the claim for priority under 35 U.S.C. 119. The certified cop	by has been received not been received
been filed in parent application, serial no; filed on _	
13. Since this application appears to be in condition for allowance except for formal matters, accordance with the practice under Ex parte Quayle, 1935 C.D. 11; 453 O.G. 213.	, prosecution as to the merits is closed in
14. Other	
·	. •

PTOL-326 (Rev. 7 - 82)

SN 1624

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The disclosure is objected to because of the following informalities: Page 1, last line, note that "causes a large occupied area for mounting" should be rephrased for clarity. Page 2, lines 5, 11, 16, 24 and page 3, line 8, note that "solving means" should be rephrased at each occurrence for clarity. Page 4, line 4, note that --is-- should follow "Fig. 1". Page 5, line 18, note that --(see Fig. 2)-- should follow "1c" for clarity; lines 20, 22, note that --(see Figs. 2, 3)-- should follow "2a" and "T<sub>1</sub>", respectively; lines 20, 24, note that --(see Figs. 2, 4)-- should follow "2b" and "T2", respectively. Page 6, line 20, note that --as shown in Fig. 2, -- should follow "Also," for consistency. In the replacement paragraph to page 10, line 17, fifth line, note that "range" should correctly be --region--.

Appropriate correction is required.

The drawings are objected to because of the following: In Figs. 2 and 3, note that element "3a" should also be labeled with --H1-- and element "4a" should also be labeled with --D1--; In Fig. 2 and 4, note that element "3b" should also be labeled with --H2-- and element "4b" should also be labeled with --D2--. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the oscillator and mixer in the IC unit (e.g. cl 5) must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

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A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Claims 3-6 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 3, note that the recitation "the first and second positions being opposite to each other with the multi-layered board interposed therebetween" is not a proper characterization.

Note from Fig. 2 that the first and second positions are not "opposite to each other" through the multi-layer board, but rather the high-frequency and demodulation units (i.e. H1, D2; H2, D1) at each position which are "opposite to each other". Clarification is needed.

In claim 4, note that it is unclear what characterizes the respective grounding layer being "arranged more proximate than the ...remainder..." (i.e. what does this read on?).

Claim 4 is found objectionable in that --respective-- should precede "deletion units" (1. 3) and --corresponding-- should precede "first remainder" (1. 4).

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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Claims 1, 2 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Kane.

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Kane (e.g. Fig. 7) discloses a double tuned frequency selective circuit comprising a printed circuit board comprising a printed circuit board comprising insulating layers (31a, 31b) sandwiching a ground layer (33). Disposed on a first surface of the printed circuit board is a first wiring electrode pattern (32) configured to form first tuning section comprising distributed inductance/capacitance. Disposed on an opposite second surface of the printed circuit board is a second wiring electrode pattern configured to form a second tuning section comprising distributed inductance/capacitance.

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Kubo et al pertains to a printed circuit board with bandpass and lowpass filters on opposite surfaces of the board.

Any inquiry concerning this communication should be directed to Benny Lee at telephone number 308-4902.

Lee/ek

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05/14/03